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BIBDATASHEET

CONFIRMATION NO. 9031

Bib Data Sheet

SERIAL NUMBER 09/699,077	FILING DATE 10/27/2000 RULE	CLASS 703	GROUP ART UNIT 2123	ATTORNEY DOCKET NO. KPO089
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** CONTINUING DATA *****
None Appl 10/28/03

** FOREIGN APPLICATIONS *****
JAPAN 14962/00 01/24/2000 *Appl 10/28/03*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 01/02/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 21	TOTAL CLAIMS 129	INDEPENDENT CLAIMS 43
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Appl 10/28/03</i>	EXAMINER'S SIGNATURE	INITIALS		

Verified and Acknowledged

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TITLE
Method and apparatus for fault simulation of semiconductor integrated circuit

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		<input type="checkbox"/> 1.16 Fees (Filing)
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